

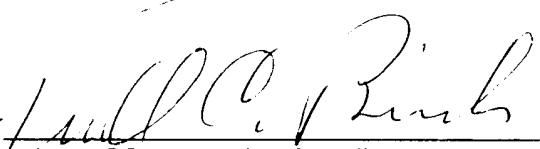
- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$84.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By


Terrell C. Birch, #19,382

TCB/RWD/lw
1152-282P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

PATENT
1152-282P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Ren Uchida Conf.: 9224
Appl. No.: 09/941,683 Group: 2829
Filed: August 30, 2001 Examiner: Patel, Paresh H.
For: TESTING METHOD AND TESTING DEVICE FOR
SEMICONDUCTOR INTEGRATED CIRCUITS

REPLY UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 22, 2003

Sir:

In reply to the Office Action dated May 27, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks being on page 5 of this paper.

Conclusion beings on page 7 of this paper.